

General

Configuration
Configuration Modes
Startup

Bitstream Properties

Enable Bitstream Compression

TRUE 

Q-

General

Configuration

Configuration Modes

Startup

Encryption

Feedback

Configuration

Configuration Setup

Tristate CCLK pin during reconfiguration

FALSE

C

Configuration Rate (MHz)

33

▼

C

The screenshot shows a software application window with a sidebar and a main configuration panel. The sidebar contains links for General, Configuration (which is highlighted), Configuration Modes, Startup, Encryption, and Feedback. The main panel has a title bar 'Configuration' and a section titled 'Configuration Setup'. It displays two configuration options: 'Tristate CCLK pin during reconfiguration' set to 'FALSE' and 'Configuration Rate (MHz)' set to '33'. A red circle highlights the '33' value in the configuration rate field.

General

Configuration

Configuration Modes

Startup

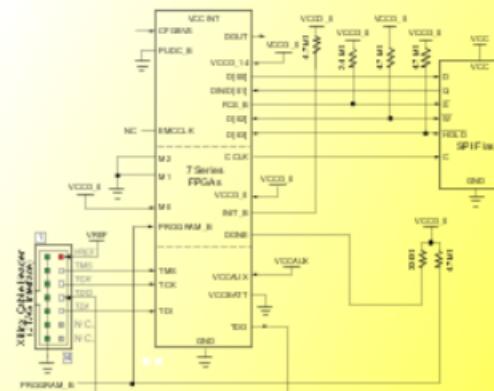
Encryption

Readback

Select up to two device configuration modes. JTAG IS always selected. Selecting mode and clicking an image displays details.

Prohibit usage of the configuration pins as user I/O and persist after config

Reset configuration mode 



Master SPI x4



Project Settings

General

Simulation

Elaboration

Synthesis

Implementation

Bitstream

> IP

Tool Settings

Project

IP Defaults

Bitstream

Specify various settings related to writing bitstream

[\(i\) Configure additional bitstream settings.](#)

▼ Write Bitstream (write_bitstream)

tcl.pre



tcl.post



-raw_bitfile



-mask_file



-no_binary_bitfile



-bin_file*

