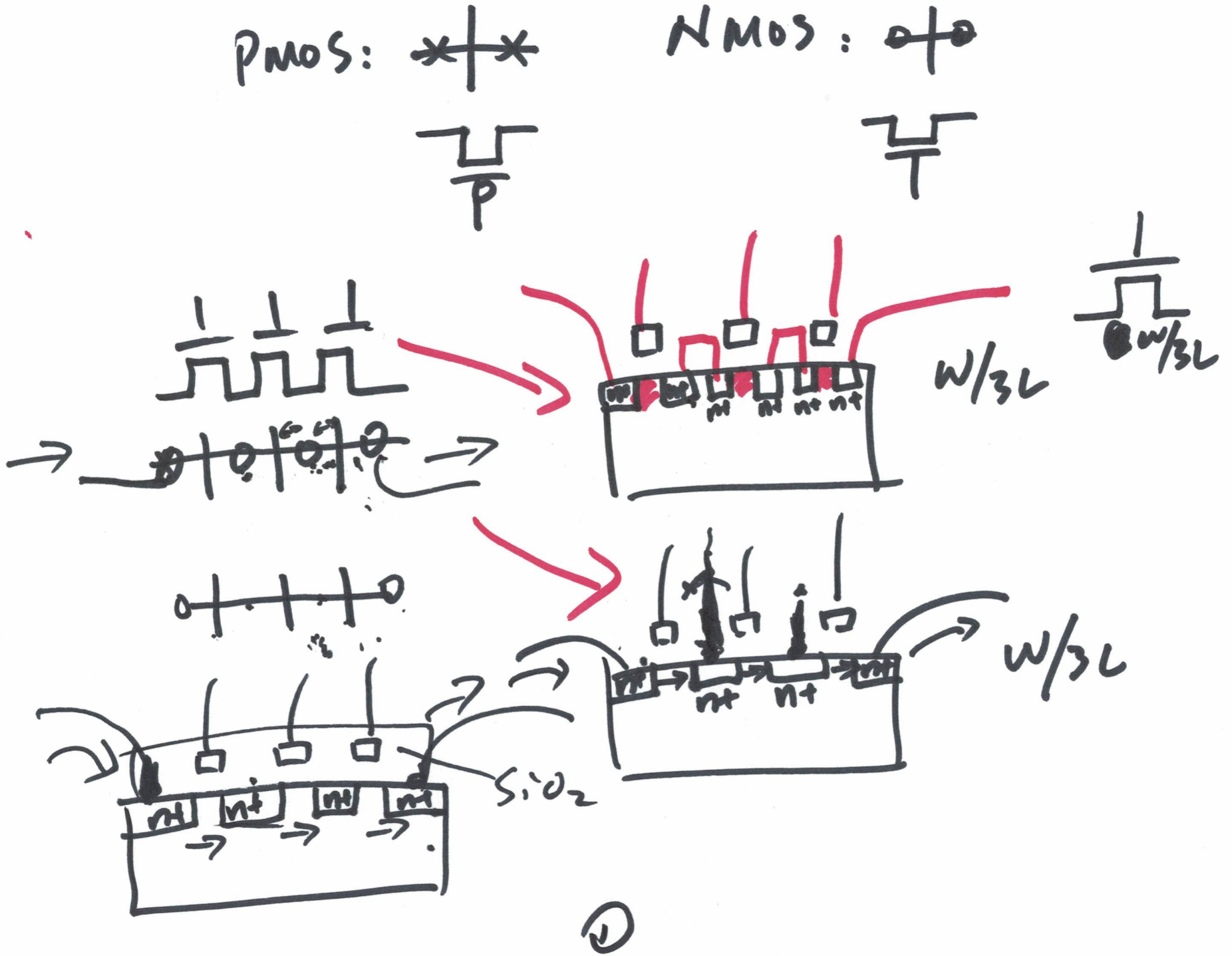
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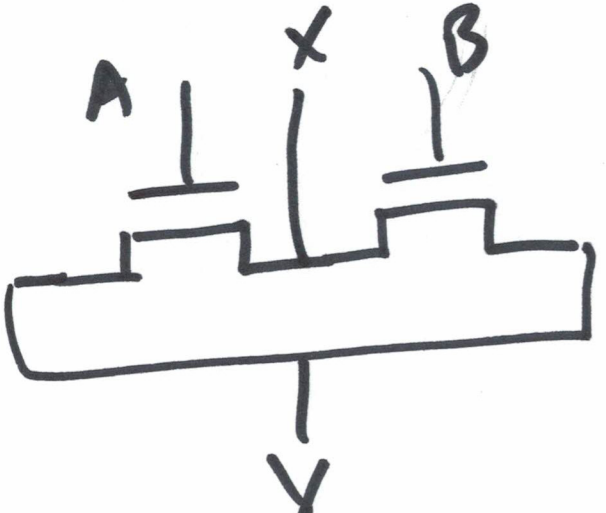
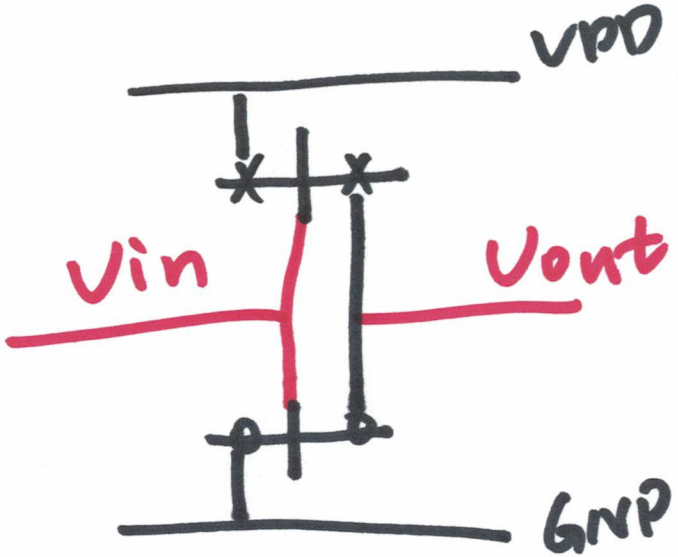
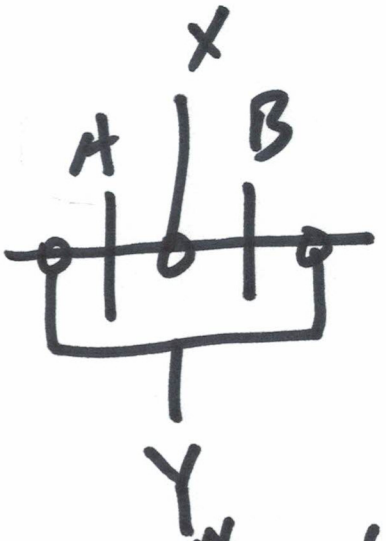
NMOS:
P
W/3L
W/3L
W/3L
SiO2
n+
p+

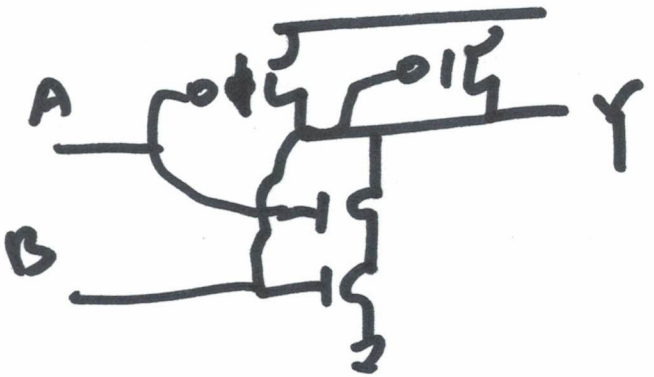


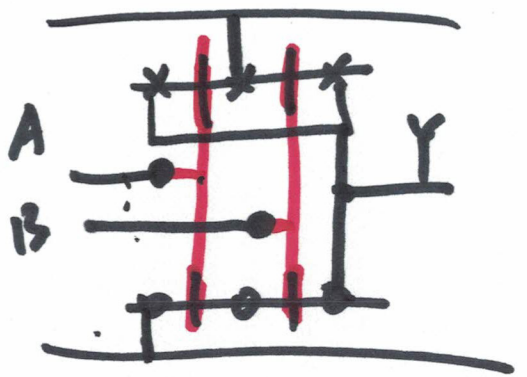
An inverter:

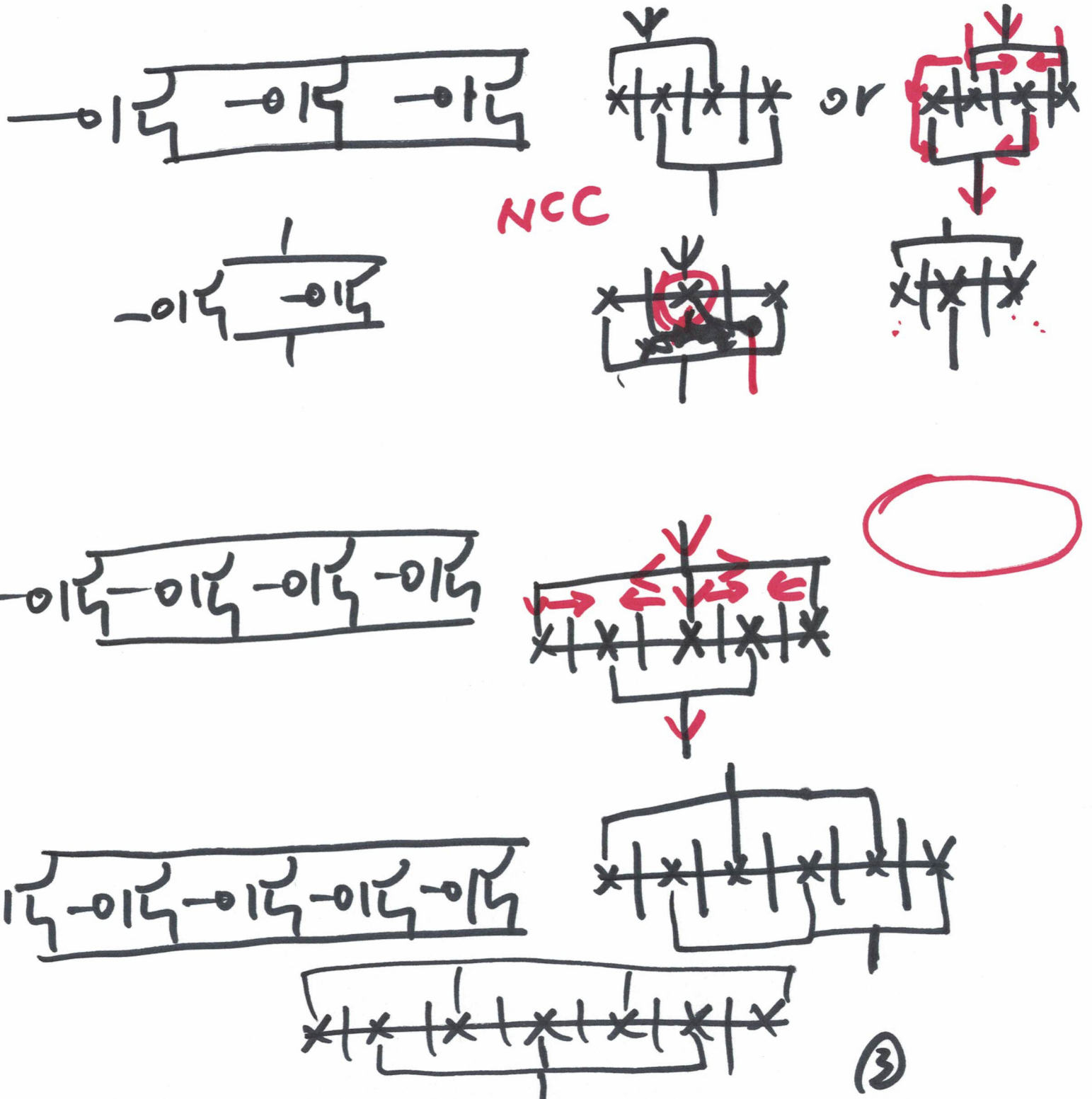
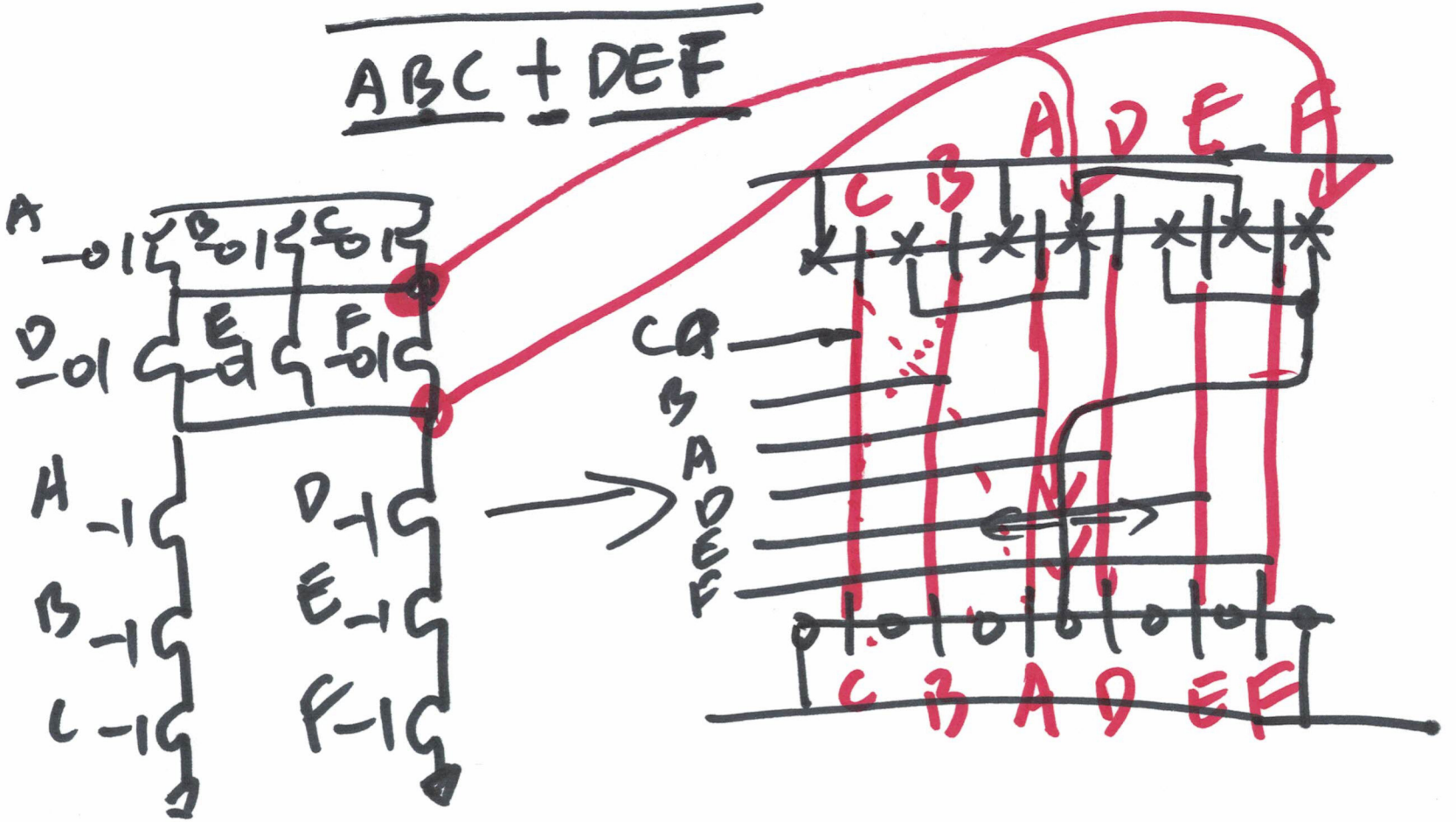




A NAND Gate






or
NCC
(3)

ABC + DEF