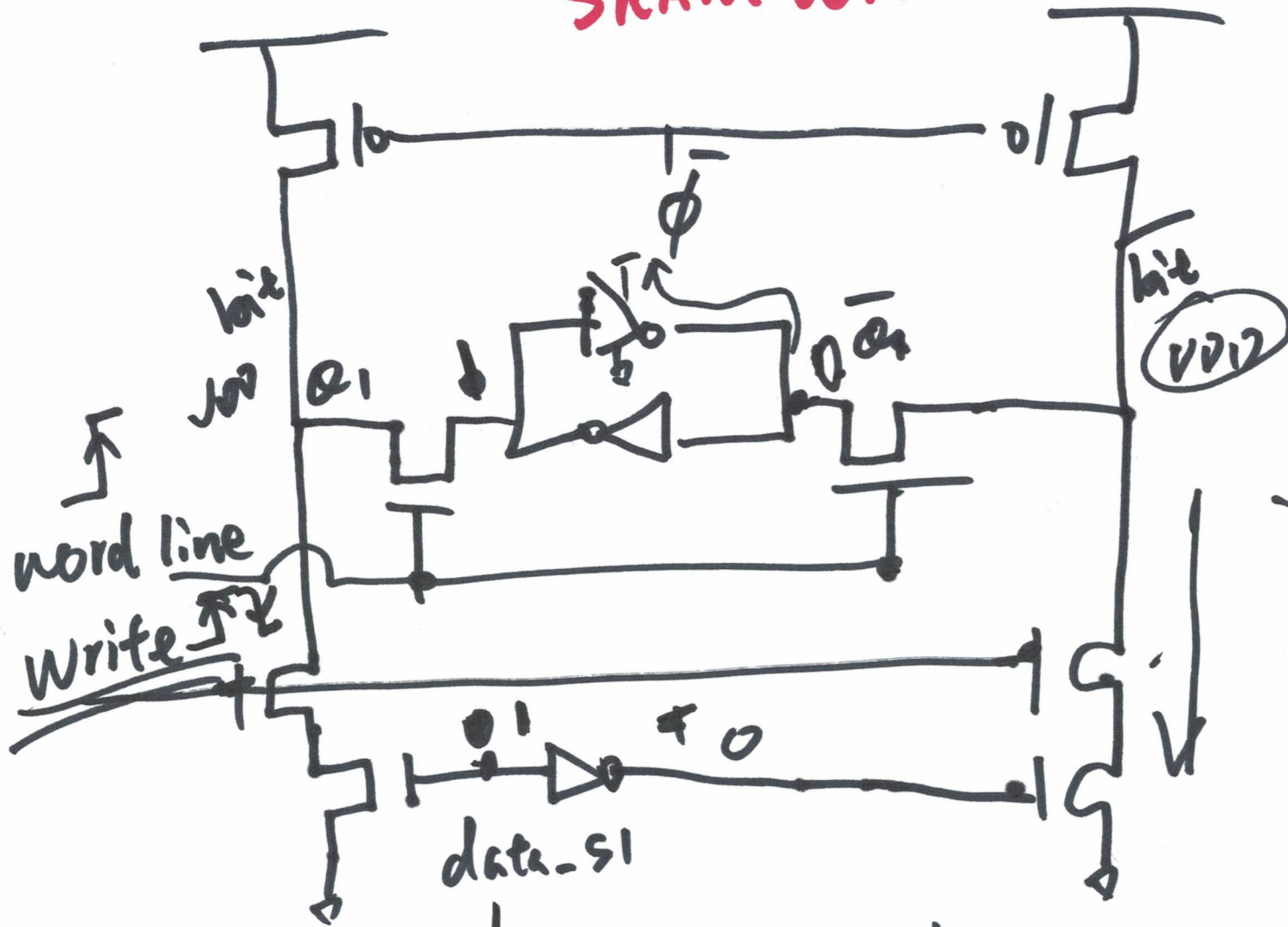


SRAM Write



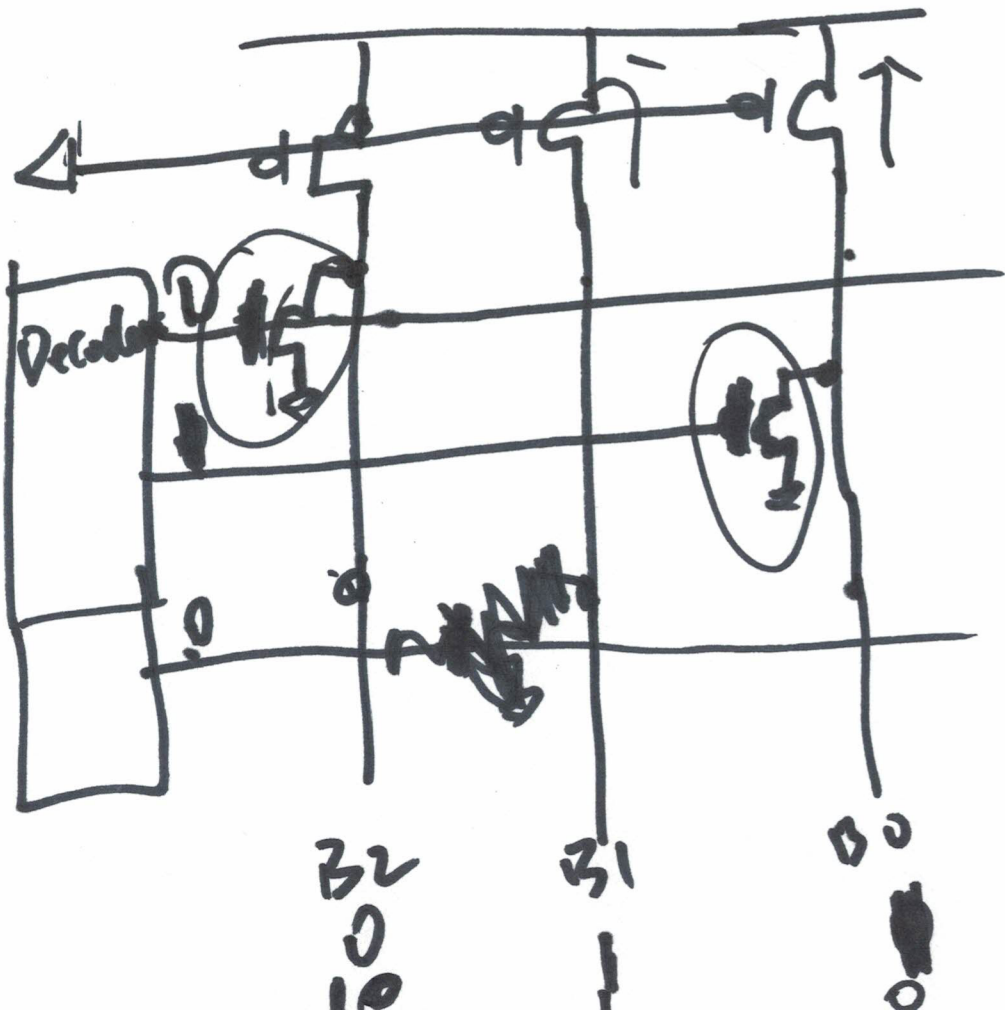
data

data-si	write
1	0

(2)

Flash → EEPROM

ROM Read-Only



(3)

Flash $\rightarrow$ ~~For~~ Floating Gate

