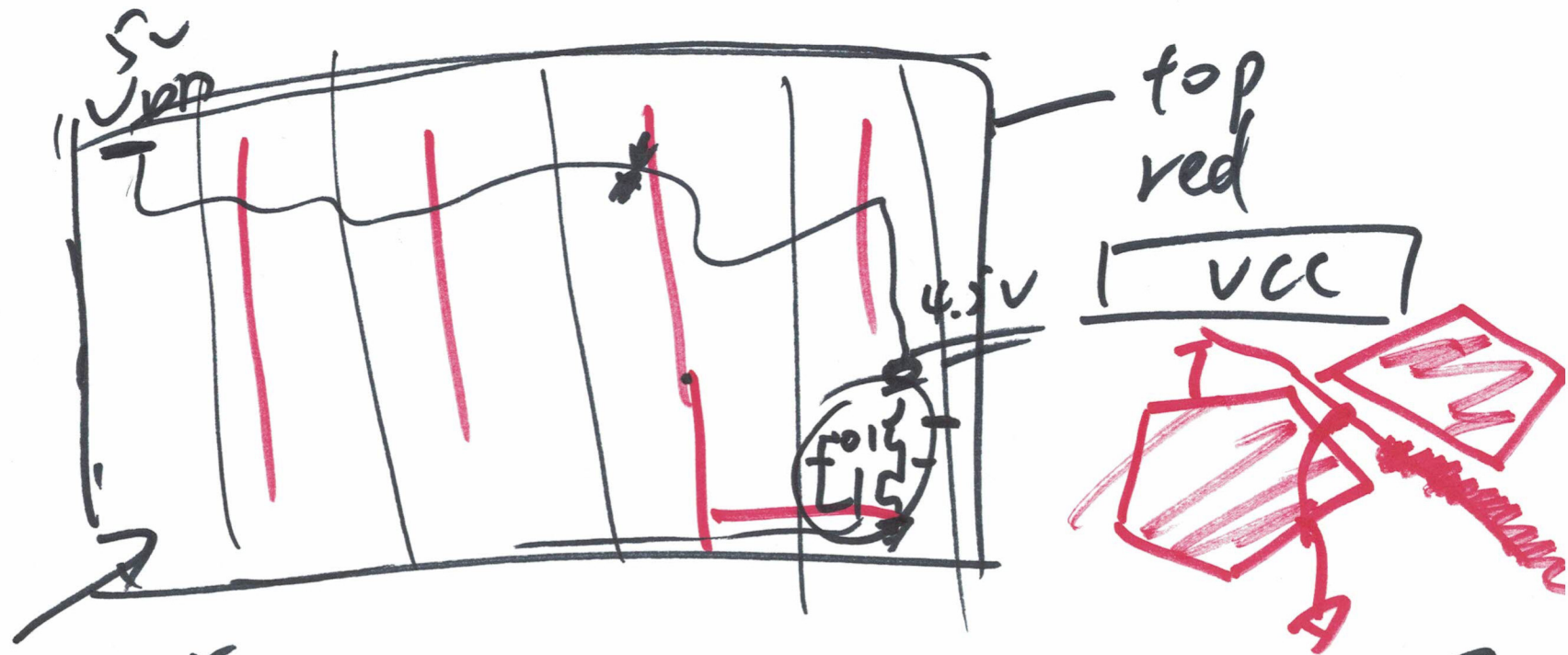
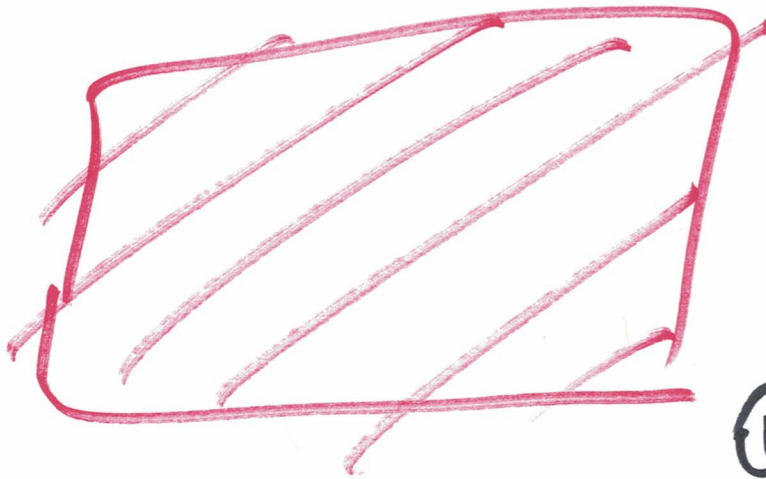




copper  
pour

rats nest



①



PDK

~~Process~~ Design Kit

foundry

2x2.5um



std cells

design rules

gates  
block  
drawing  
material

3

Static random  
Access mem

SRAM

Dynamic

~~DRAM~~, ROM, flash

less power

faster

insensitive  
to noise

high cost

used inside CPU  
for cache

2-40 ns

6-transistor  
circuit

cards (off-chip)

60-100 ns

1 transistor  
1 cap

# cell of SRAM

