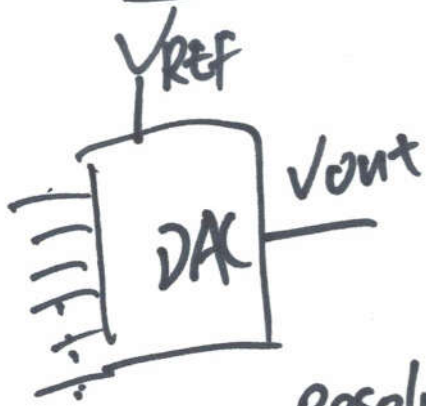


Example: Find the number of input combinations, voltage values for 1 LSB, and full-scale voltage generated for a 3-bit, 8-bit, and 16-bit DAC. $V_{REF} = 5V$

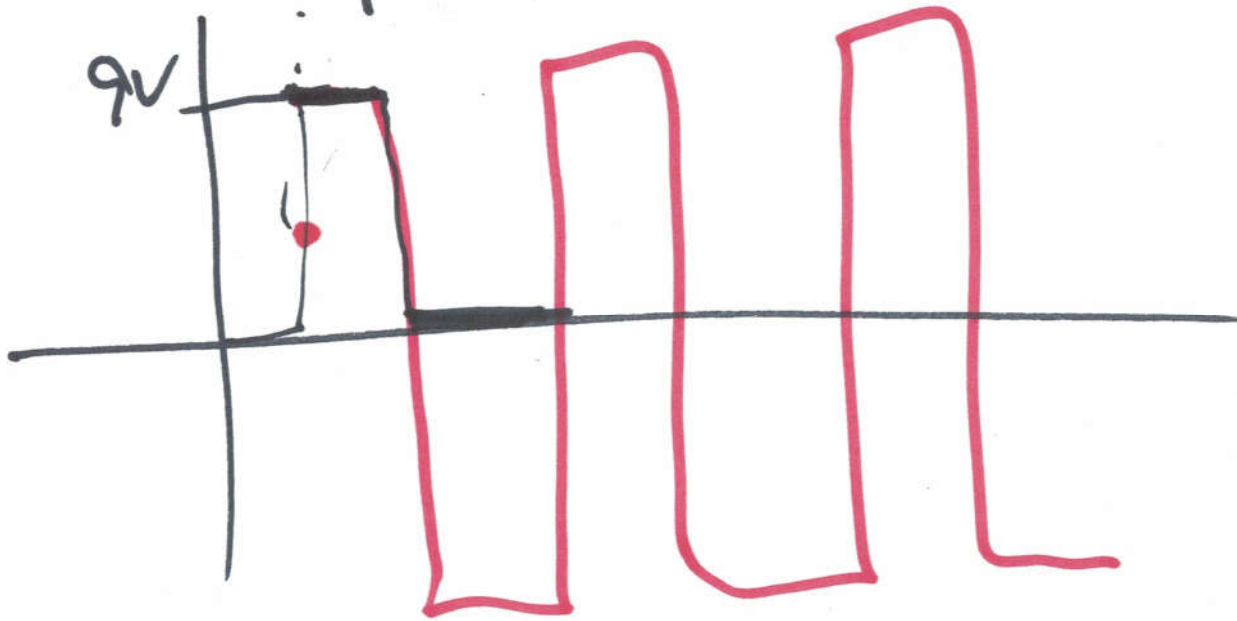
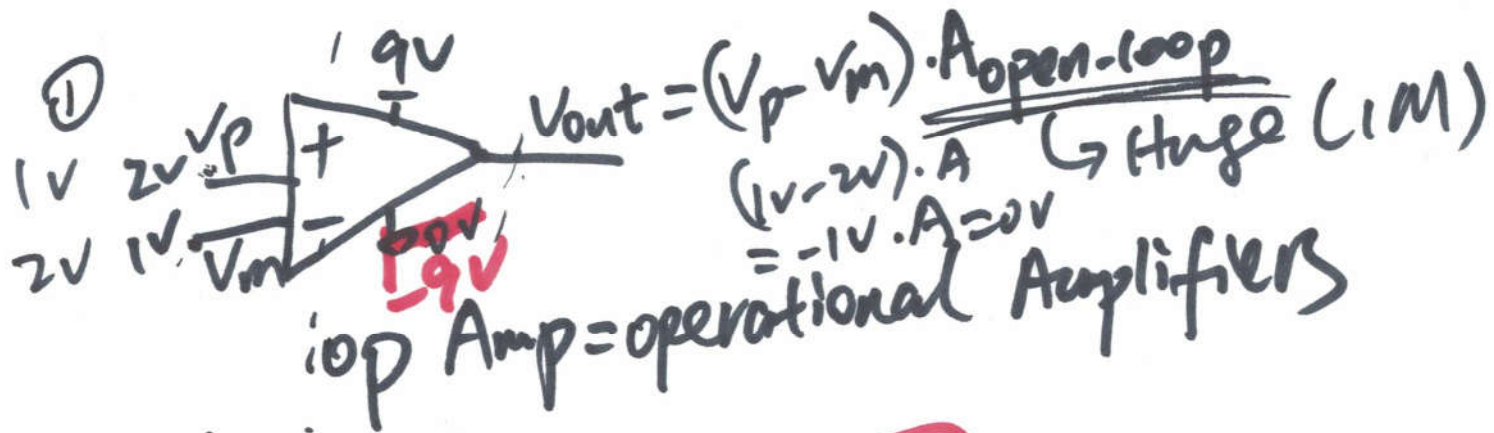


Resolution	# Input Combinations	1 LSB	$V_{FS} = \frac{2^n - 1}{2^n} \cdot V_{REF}$
3-bit	$2^3 = 8$	$\frac{5V}{2^3} = \frac{5}{8} = 0.625V$	$\frac{7}{8} \cdot 5V =$
8-bit	$2^8 = 256$	$\frac{5V}{2^8} = \frac{5V}{256} =$	$\frac{255}{256} \cdot 5V =$
16-bit	$2^{16} = 65536$	$\frac{5}{2^{16}} = \frac{5V}{65536} =$	$\frac{65535}{65536} \cdot 5V =$

CDC
ADC

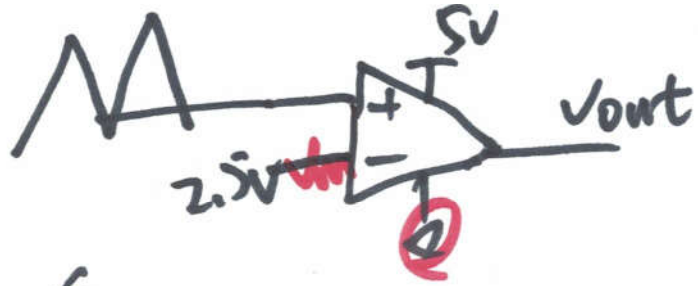
1 Byte = 8 bits
 $2^{10} = 1024 = 1K$
 $2^{20} = 1024 \cdot 1024 = 1MEG$
 $2^{30} = 1.073B$

LTSpice
 Pspice
 Nspice
 Hspice
 (Specere)



Comparator.

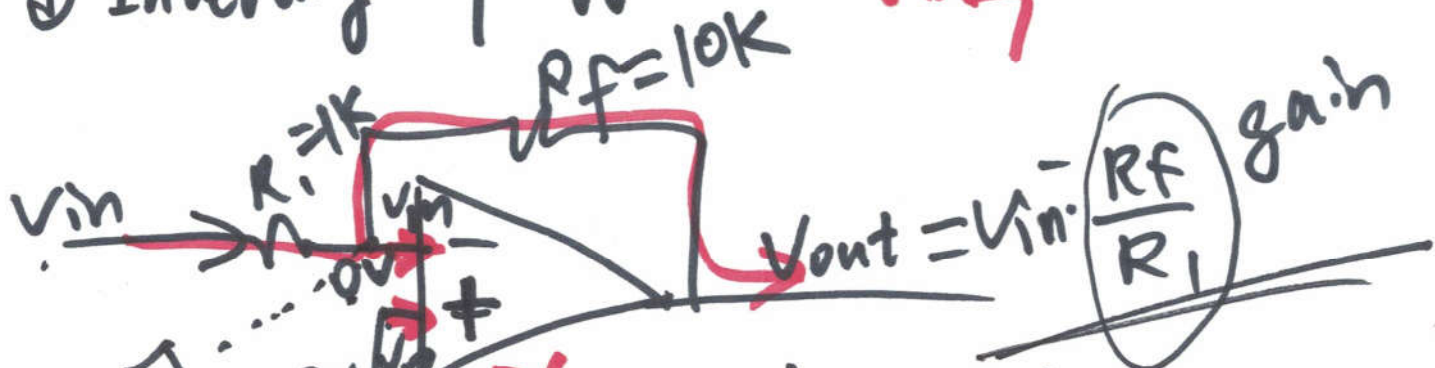
②



3

① Inverting Topology

THS



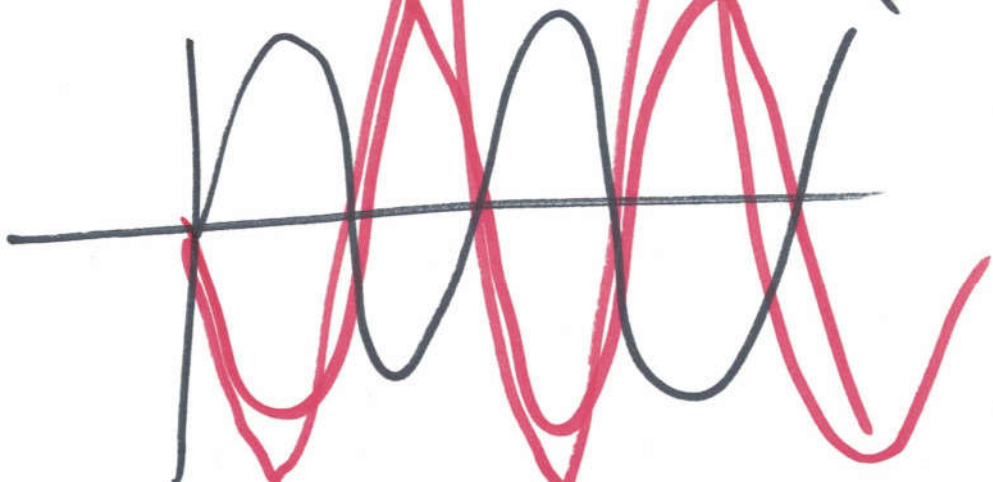
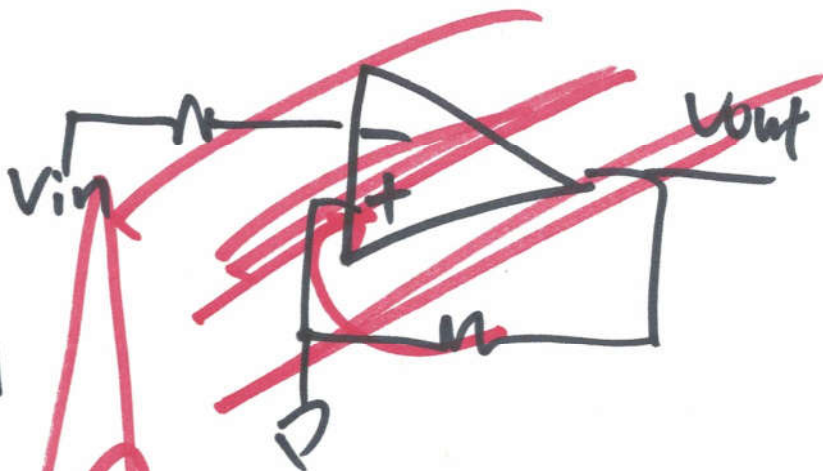
$$(V_p - V_m) \cdot A = V_{out}$$

$$\frac{V_{in} - 0}{R_1} = \frac{0 - V_{out}}{R_f}$$

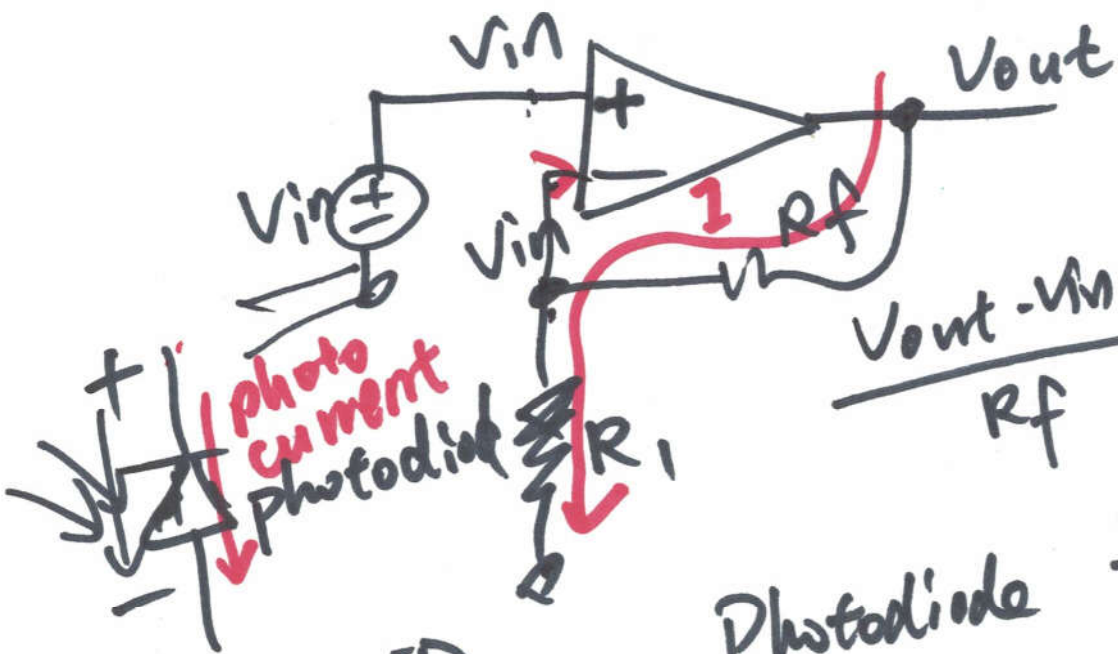
THA

$$\frac{R_f}{R_1} = \frac{-V_{out}}{V_{in}}$$

$$\frac{V_{out}}{V_{in}} = -\frac{R_f}{R_1}$$



(2) Non-inverting. Topology



$$\frac{V_{out} - V_{in}}{R_f} = \frac{V_{in} - 0}{R_1}$$

$$\frac{R_f}{R_1} = \frac{V_{out} - V_{in}}{V_{in} - 0}$$

$$= \frac{V_{out}}{V_{in}} - 1$$

