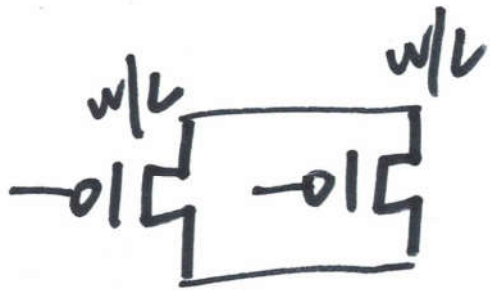
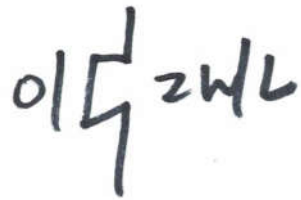


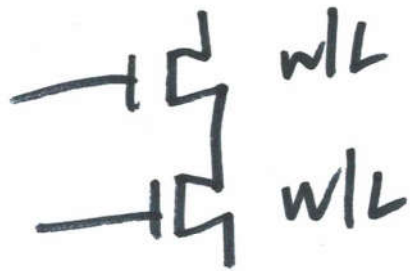
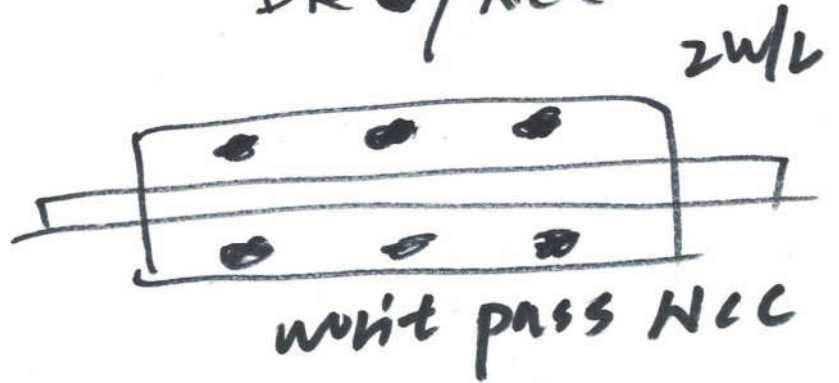
①



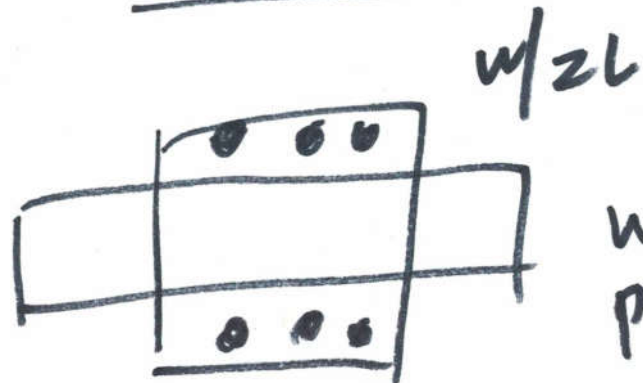
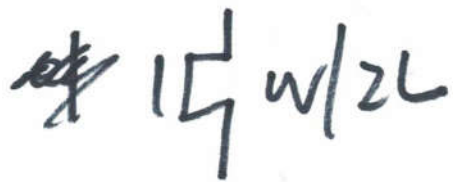
\Rightarrow



\Rightarrow



\Rightarrow

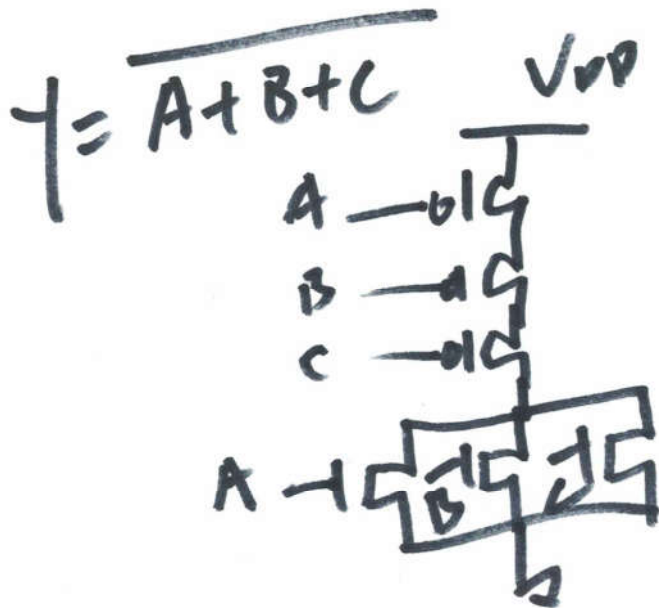
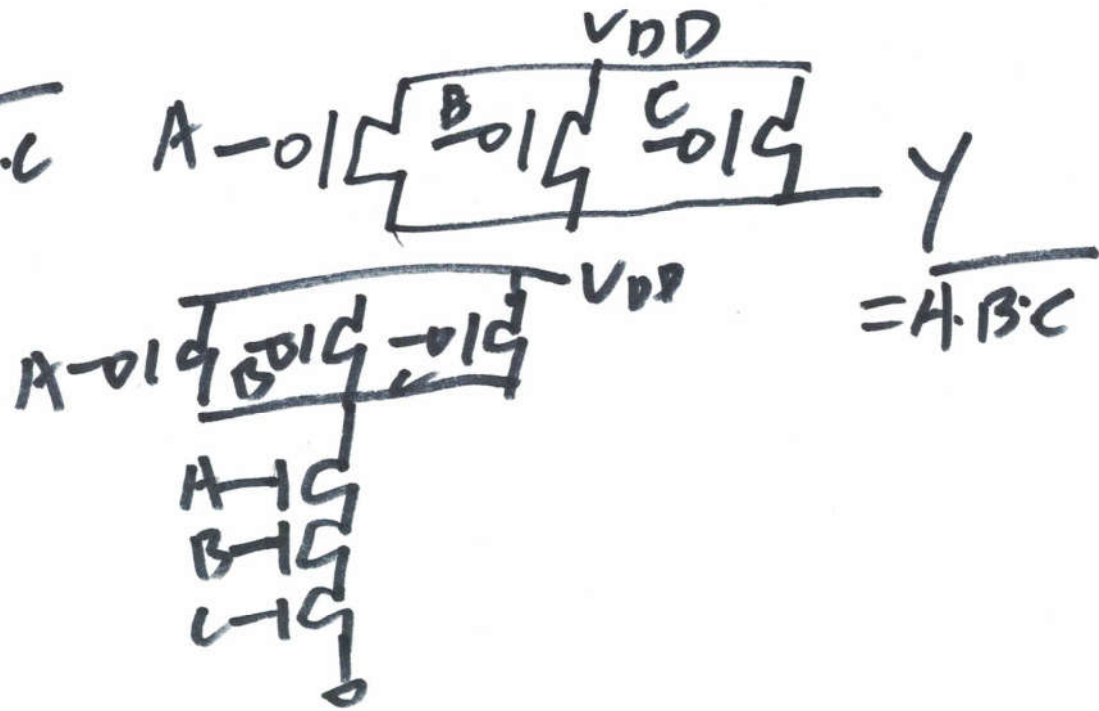
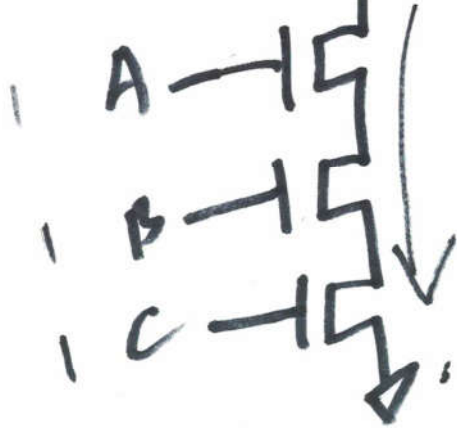


②

② Complex CMOS logic gates

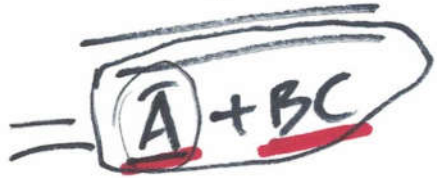
$$Y = \overline{A \cdot B \cdot C}$$

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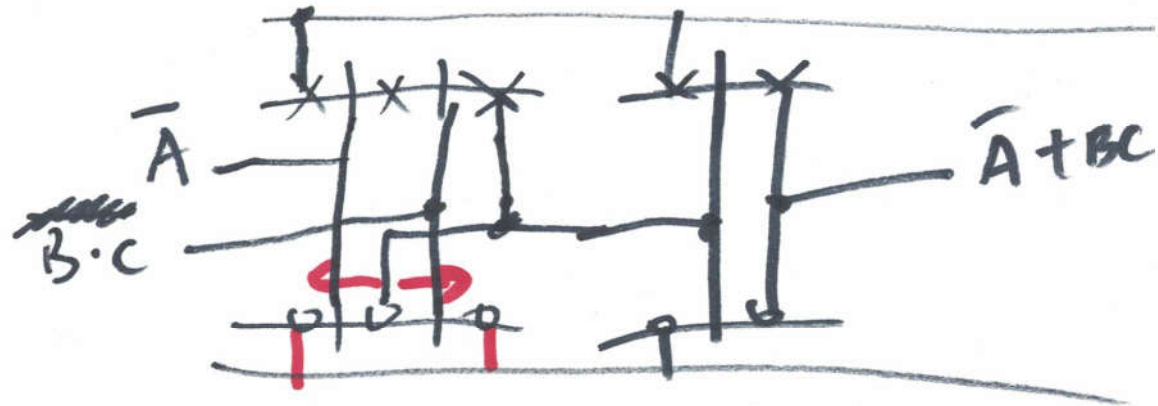
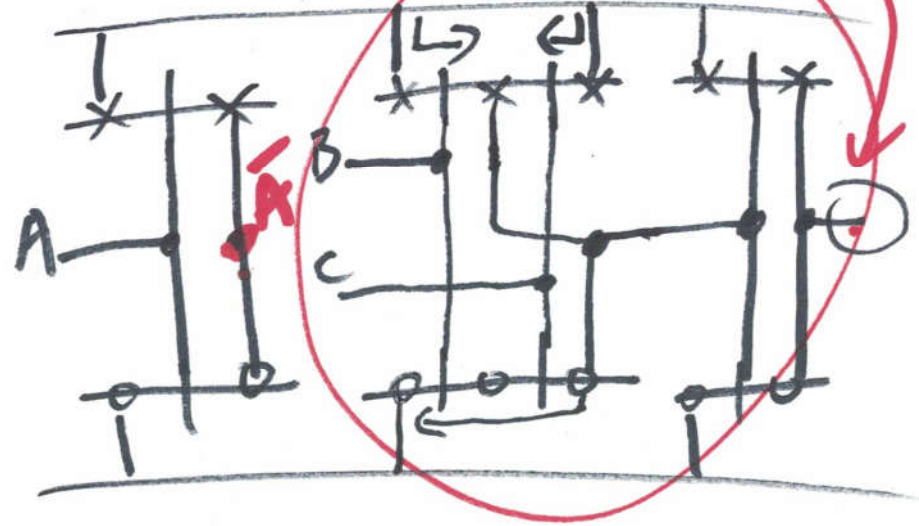
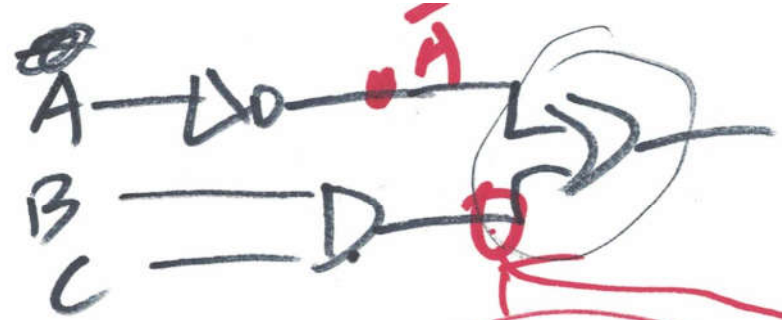
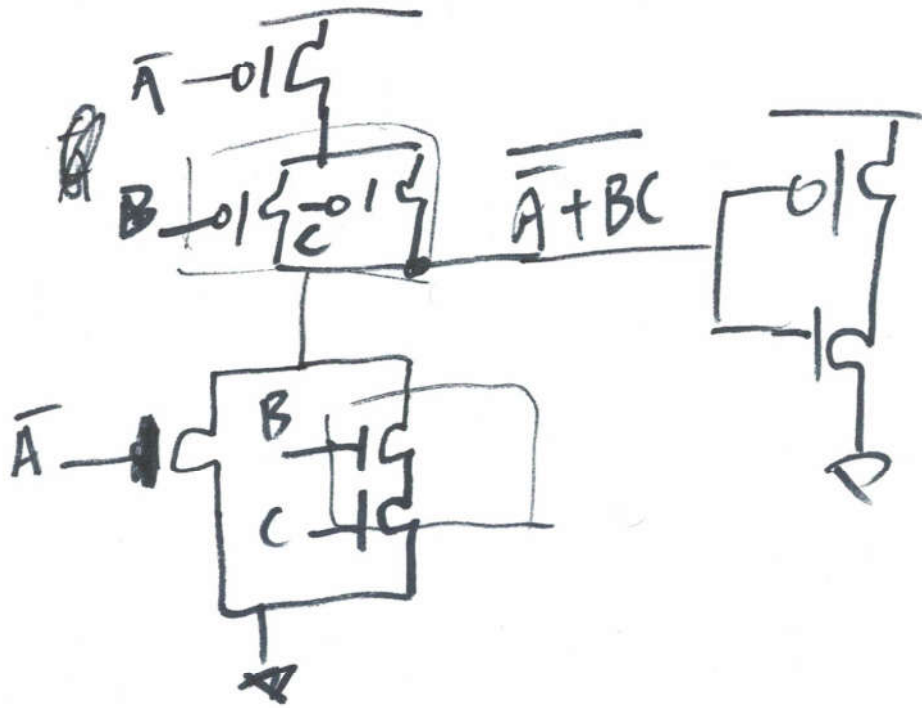


⇒ AND-OR-Invert (AOI logic) to implement any arbitrary logics.

$$Y = \bar{A} + BC$$



Method 1:



(3)

