	111	S3
×	10[1]	S4
	111	
	000	
	111	
	100011	

same C

S5	C	S6	C	
0000 0000				load S5, 0
				sro S4
0000 0111				add S5, S3
00000011	[1]	00000000	[0]	sra S5
00000011		10000000		sra S6
				sro S4
00000001	[1]			sra S5
		11000000	[0]	sra S6
				sro S4
f 00000111				
00001000		11000000		add S5, S3
				sra S5

$$\frac{100\,000000 \text{ counts/s}}{9600 \text{ bits/s}}$$

$$= \frac{100\,\text{M}}{9600} \frac{\text{counts}}{\text{bit}}$$