

```
TCCR5A |= (1 << COM5B1);
```

```
OCR5B = 42;
```

```
mask = 0b00000001;
```

```
letter = buf[i];
```

```
tmr3ISRState = TX_DATA_BITS;
```

```
if (mask == 0)
    tmr3ISRState = TX_STOP_BITS;
    TCCR5A &= ~(1 << COM5B1);
else
```

```
{
```

```
}
```

①

8 bits

mask =

0b00000001

```
if (letter & mask)
```

```
TCCR5A &= ~
```

```
(1 << COM5B1);
```

```
else
```

```
{ TCCR5A |= (
```

```
OCR5B = 42;
```

```
}
```

CASE TX_STOP BITS:

if (bufc:1 == '1\0')

transRState = TX_START_BITS;
checkSum = true;

elseif (checkSum)

checkSum = false;
transMidState = false
transRState = IDLE;

else

{ transRState = TX_START_BITS; }

i++;

②

+ transmitStart false

TX-IDLE

transmitStart true

index = 0

TX-START-BITS

logic 0

letter = buf[index]

TX-DATA-BITS

mask $\neq$ 0
mask $\leq$ 1

mask = 0

logic 1

TX-STOP-BITS

Next byte
or '10' sent
↓
checksum

checksum sent
→ IDLE

