

① right-justified (10-bit precision)
 $\text{vint16.t} = (\text{ADCH} \ll 8) \mid \text{ADCL}$

② left-justified (10-bit precision)
 $= (\text{ADCH} \ll 2) \mid (\text{ADCL} \gg 6)$

read
 ADCL first
 then ADCH

$$\frac{1000000000}{500.5}$$

8-bit precision

$\text{vint8.t} \text{ result} = \underline{\underline{\text{ADCH}}};$

$$\frac{1000000}{500} = 5.5\% \\ = 2.5V$$