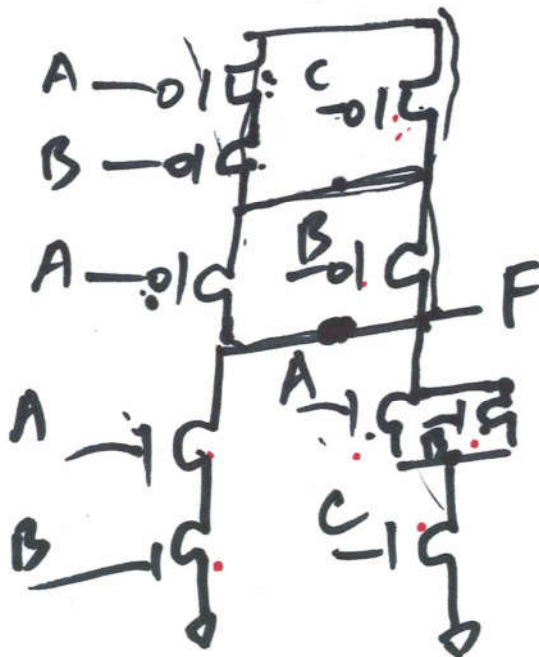
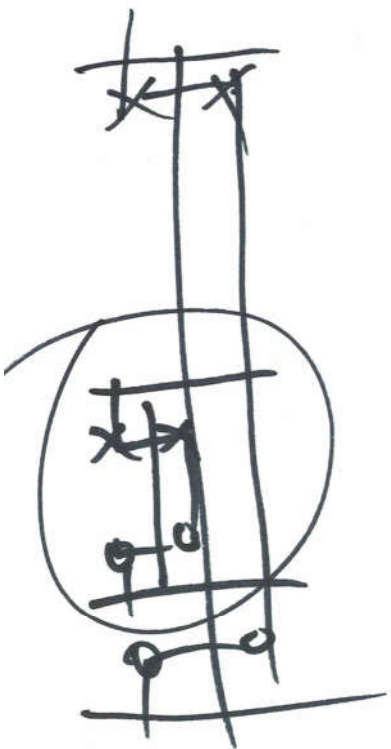
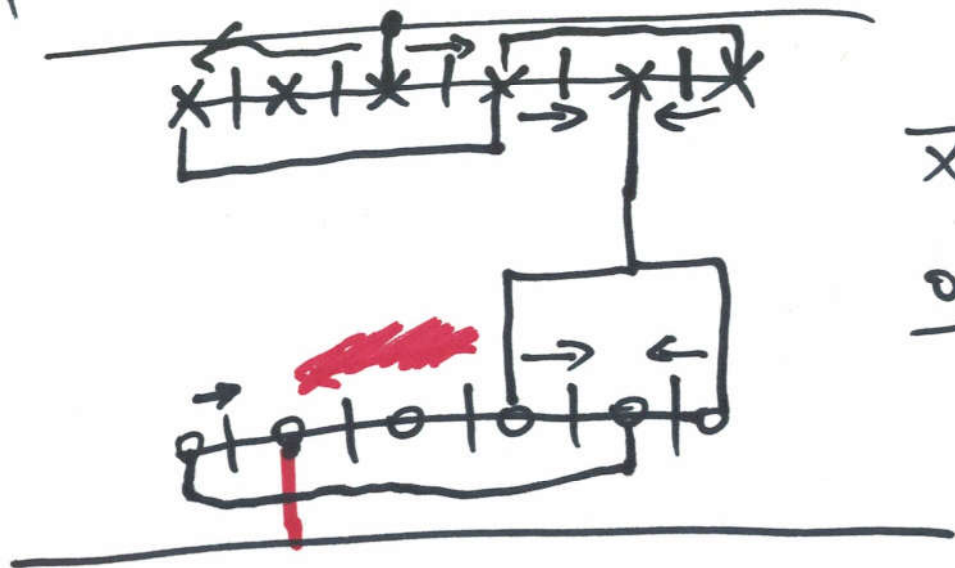


$$F = \overline{(AB+AC+BC)} = \overline{AB} + \overline{(A+B)C}$$



A	B	C	<del>F</del>	F
0	0	0	1	✓
0	0	1	1	✓
0	1	0	1	✓
0	1	1	1	✓
1	0	0	0	✓
1	0	1	0	✓
1	1	0	0	✓
1	1	1	0	✓

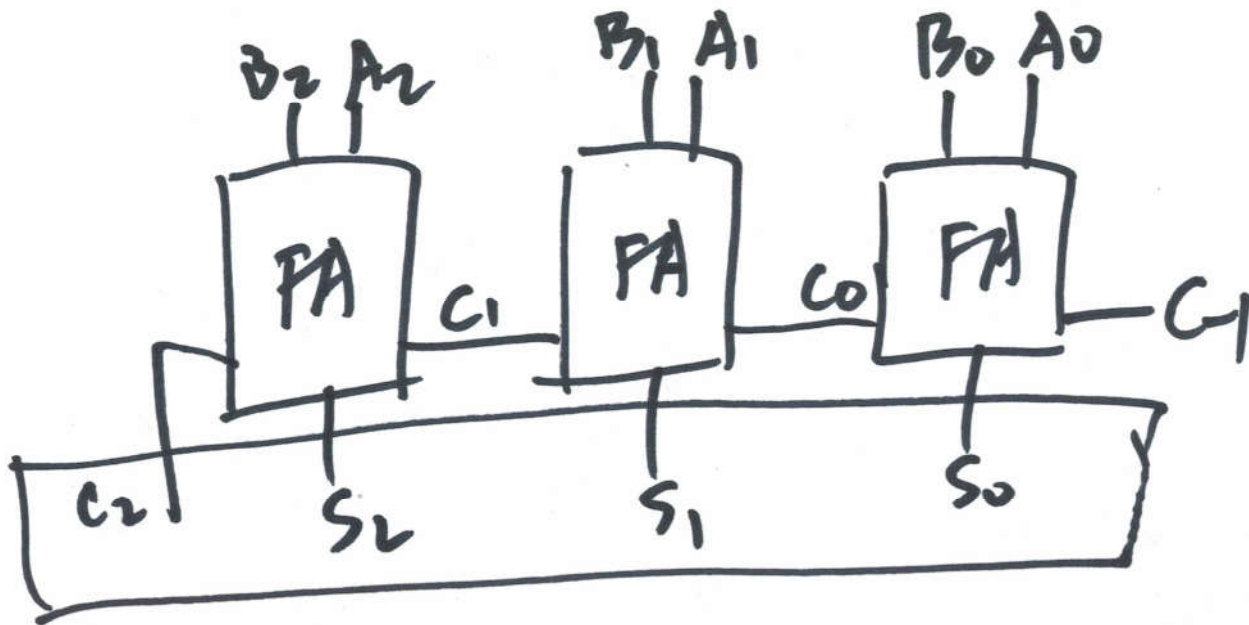
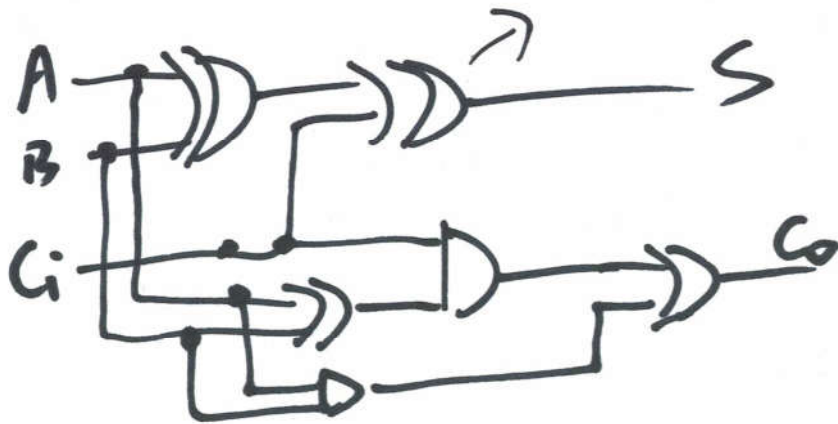
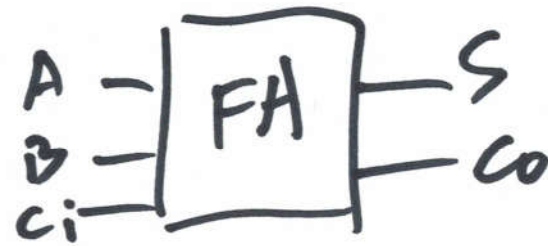


$\overline{XX}$   
 $\overline{00}$

(1)

# FAC Full Adder)

$$\begin{cases} S = A \oplus B \oplus C \\ C_o = (A+B)C_i + AB \end{cases}$$



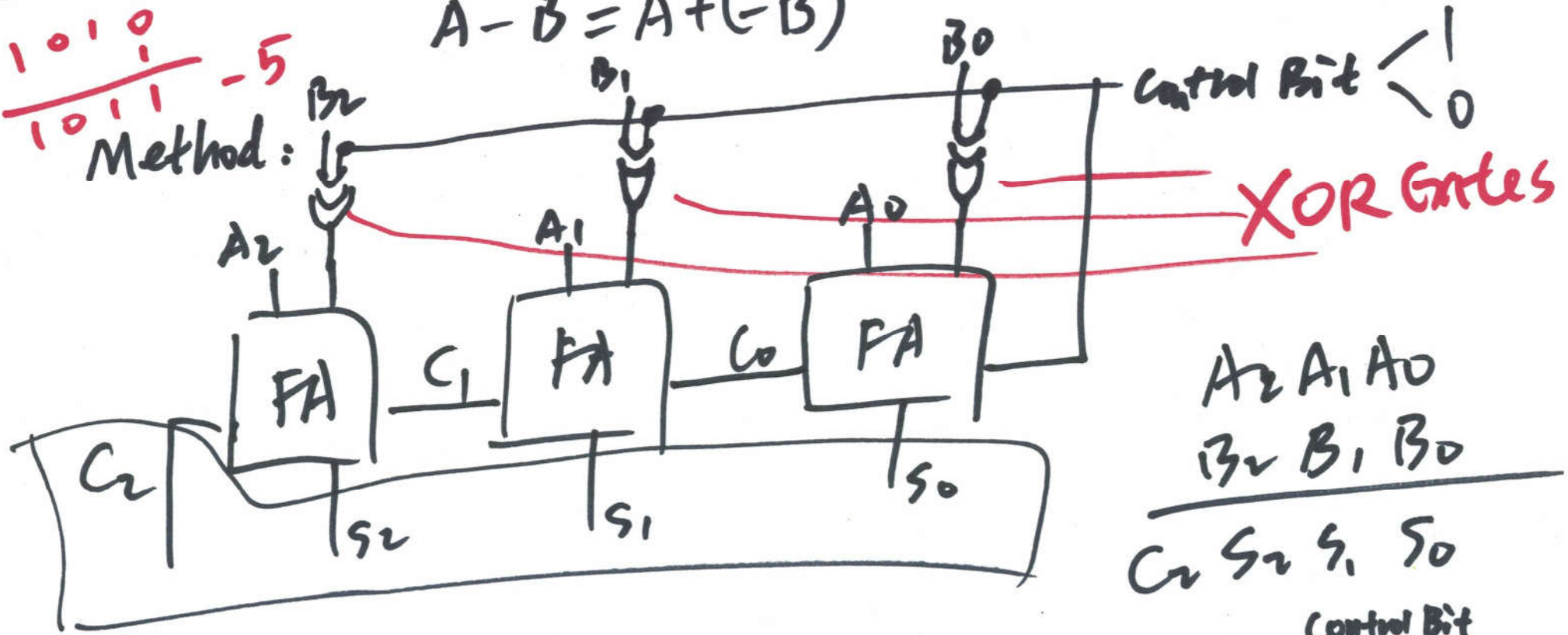
(2)

2's complement: invert all the bits and add one to the result

0101 5  
 1010 -5  
 -----  
 1011

B 0 ⊕ 1 = 1  
 1 ⊕ 1 = 0

$$A - B = A + (-B)$$

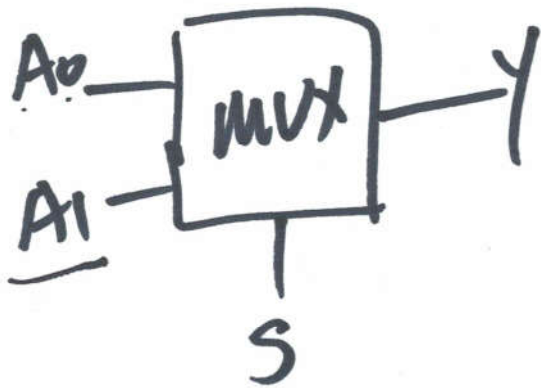
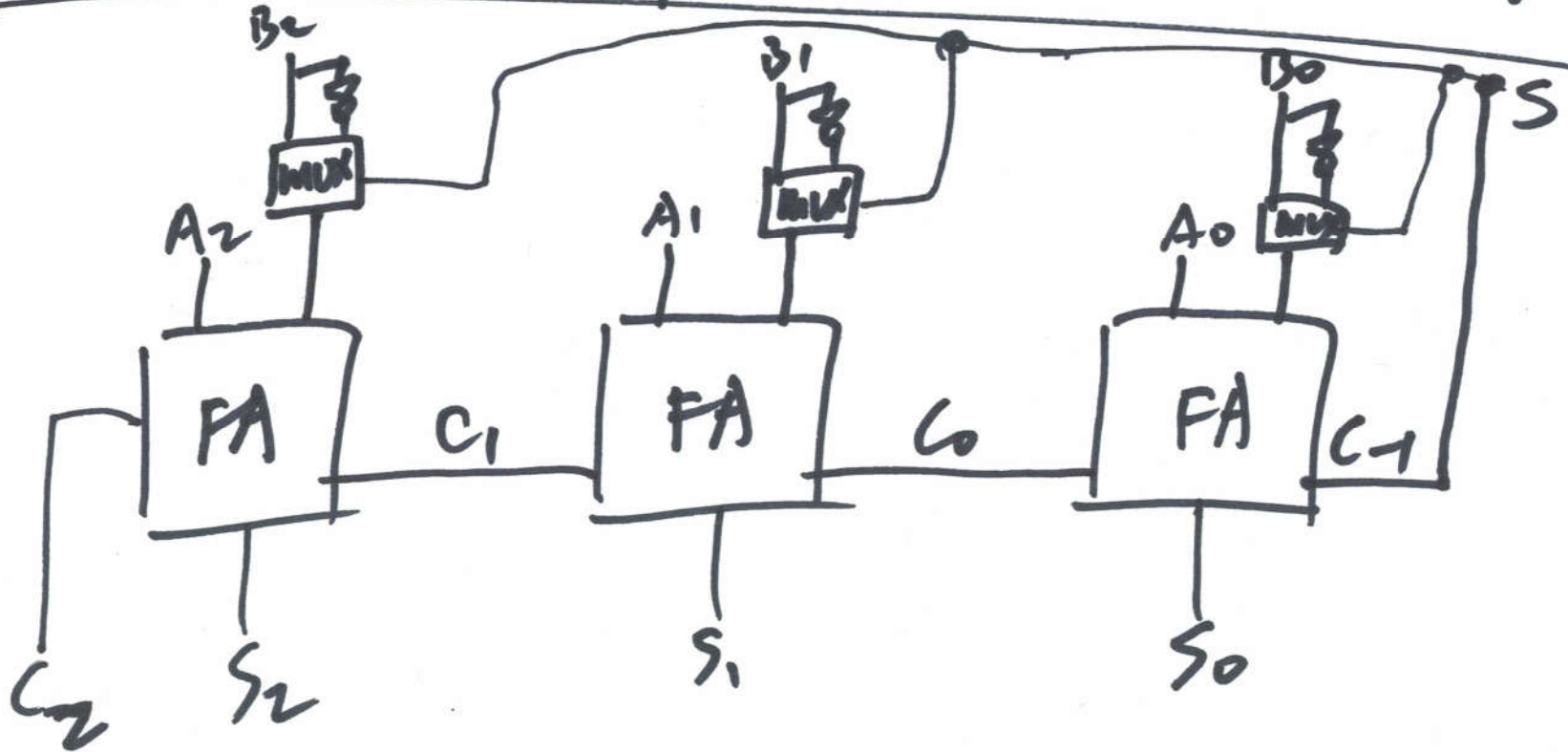


Control Bit  $\begin{matrix} 1 \\ \diagdown \\ 0 \end{matrix}$

XOR Gates

$A_2$	$A_1$	$A_0$	
$B_2$	$B_1$	$B_0$	
$C_2$	$S_2$	$S_1$	$S_0$
	Control Bit		
$B$	$0 \oplus 0$	$0$	
	$1 \oplus 0$	$1$	

Use a MUX to implement the subtractor's logic



S	A <sub>1</sub>	A <sub>0</sub>	Y
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1

