



Course Syllabus
CE 351 Microcontrollers (3-Credit)

1. Professor:

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Office Hours: Office hours will be on Microsoft Teams (virtual) for Fall 2020 due to the pandemic (the office area has limited access to students)

The Office hours will be MWF 9 am - 11 am, 8/24/2020 - 11/25/2020. The Microsoft Teams link: [Join Microsoft Teams Meeting](#)

Send me an email if you need to talk to me face-to-face, I'll come out to meet with you if I can.

2. Course Overview

This course introduces students to the typical architecture and internal units of a microcontroller and its application to analog and digital embedded systems. Topics include: Programming in C, instruction and register sets and input/output for a given microcontroller family, interfacing of analogue and digital signals and devices, serial communications, interrupts and service routines, process priority, and timing analysis. Some industrial standard microcontrollers such as ESP8266, ESP32, ST32 ARM Core, TI MSP430, and others will be used in the final project.

3. Schedule for Fall 2020 due to the COVID-19 pandemic

Fall 2020 Schedule:

M Aug 24 - begin classes

W Nov 25 - end classes

R Nov 26 thru Su Nov 29 - Thanksgiving break

M Nov 30 thru F Dec 4 - Finals

I am allowed to fit a maximum number of 15 students in SFH2771 at one time. Group 2 currently has 9 students, if you strongly feel that you should join Group 2 instead of Group 1, let me know.

The labs for Group 1 serves as pre-requisites for the labs for Group 2.

Group 1&2, Monday, 11:15-12:10, EBH010, Capacity: 35	Group 1, Wednesday, 11:15-12:10, SFH2771, Capacity: 15	Group 2, Friday, 11:15-12:10, SFH2771, Capacity: 15
All	Monica Fuson John Hitti Lekeesha James Nick Llarena Arias Cesar Molina Braden Morrow Roxie Sandoval Nic Theobald Aaron Toledo Sophie Turner KeNeda Randall	Humberto Arredondo Perez Tyrone Bracker-Yazzie Jesse Duran James Ferguson Zackary Ghalayini Taylor Nakai Scott Orban Zane Sauer Cheyenne Tucson Trevor Sharp Ryan Jeanes

4. Course Topics and Schedule (the tutorials being used for Group 1 and 2 are different, please visit www.yilectronics.com, under the tag ‘Teaching’ to find the instructions for your group.

Group 1:

<i>Week 1-2</i>	Power Supply Circuit for MCUs
<i>Week 3-4</i>	Arduino Basics
<i>Week 5-6</i>	Display, Sensors
<i>Week 7-8</i>	PID Controllers, IMUs
<i>Week 9-10</i>	Smart Car Design
<i>Week 11-14</i>	The Maze Solver Challenge

Group 2:

<i>Week 1-2</i>	Power Supply Circuit for MCUs
<i>Week 3-4</i>	ATMega 328 Advanced Applications (IOT, ESP8266, and Thingspeak)
<i>Week 5-6</i>	Advanced IoT Devices (ESP32)
<i>Week 7-8</i>	STM32 Microcontrollers
<i>Week 9-10</i>	MSP430 Microcontrollers
<i>Week 11-14</i>	The Maze Solver Challenge

5. Course Learning Outcomes (with associated ABET criteria):

After completing CE 351 students will be able to:

- Design an embedded system using microcontrollers. (1, 2)
- Using microcontrollers for data acquisition. (1, 2)
- Using microcontrollers for signal processing. (1, 2, 6)
- Design a printed circuit board for industrial applications. (1, 2, 6)
- Design a GUI to communication with microcontrollers. (1, 2)

6. Engineering Program Student Learning Outcomes (ABET criteria)

1. an ability to identify, formulate, and solve complex engineering problems by applying principles of engineering, science, and mathematics.
2. an ability to apply engineering design to produce solutions that meet specified needs with consideration of public health, safety, and welfare, as well as global, cultural social, environmental, and economic factors.
3. an ability to communicate effectively with a range of audiences.

4. an ability to recognize ethical and professional responsibilities in engineering situations and make informed judgments, which must consider the impact of engineering solutions in global, economic, environmental, and societal contexts.

5. an ability to function effectively on a team whose members together provide leadership, create a collaborative and inclusive environment, establish goals, plan tasks, and meet objectives.

6. an ability to develop and conduct appropriate experimentation, analyze and interpret data, and use engineering judgement to draw conclusions.

7. an ability to acquire and apply new knowledge as needed, using appropriate learning strategies.

7. Prerequisite

CE 241/CE341 Fundamentals of Computer Logic, at least C- OR ENGR338 (2018 and before), at least C-.

8. Textbook

No Textbook is required for this class. Visit www.yilelectronics.com for tutorials and instructions.

9. Grading, Homework assignments, Quizzes, and Exams

Homework assignments and quizzes 60%, project report/presentation 20%, Final 20%.

A: 93-100, A-: 90-92, B+: 87-89, B: 83-86, B-: 80-82, C+: 77-79, C: 73-76, C-: 70-72, D+: 67-69, D: 63-66, D-: 60-62, F: <60

Homework assignments are lab reports that you should upload to the website. (Instructions for how to do this will be available to you).

Quizzes will be done in class. I'll notify you 1 week prior to the day that has a quiz.

There is only one final exam for this course. No midterm and other exams.

10. Policies

Regularly being tardy for lectures, leaving in the middle of lectures, or earlier from lectures is unacceptable without prior consent of the instructor.

Cheating or plagiarism will result in an automatic F grade in the course (so do your own homework and projects).

****"Fort Lewis College is committed to providing all students a liberal arts education through a personalized learning environment. If you think you have or you do have a documented disability which will need reasonable academic accommodations, and/or if you are a Veteran who may need services, please contact the Disability Services Office, 280 Noble Hall, 970-247-7383, disabilityservices@fortlewis.edu for an appointment as soon as possible."