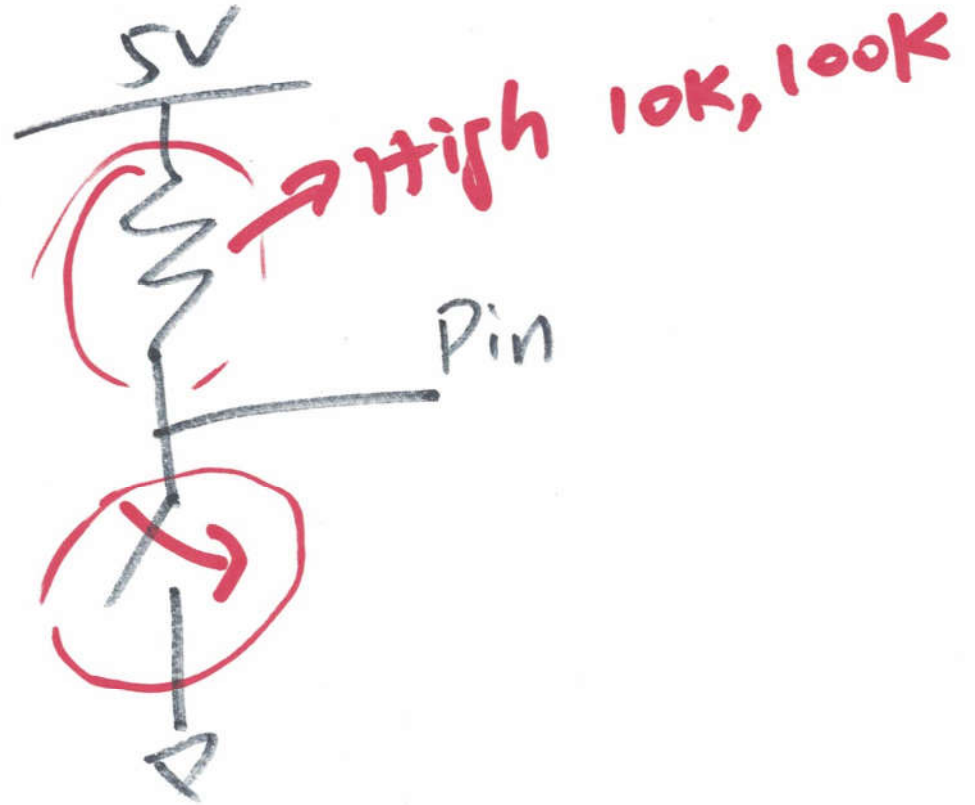
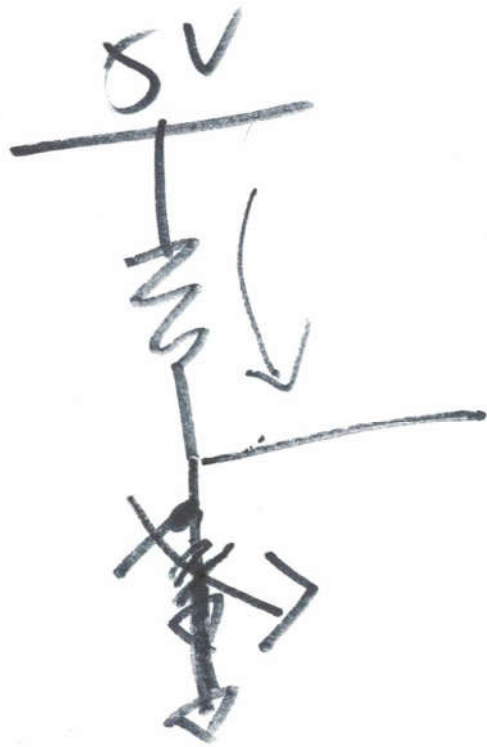


Pull-up

Pull-down resistors

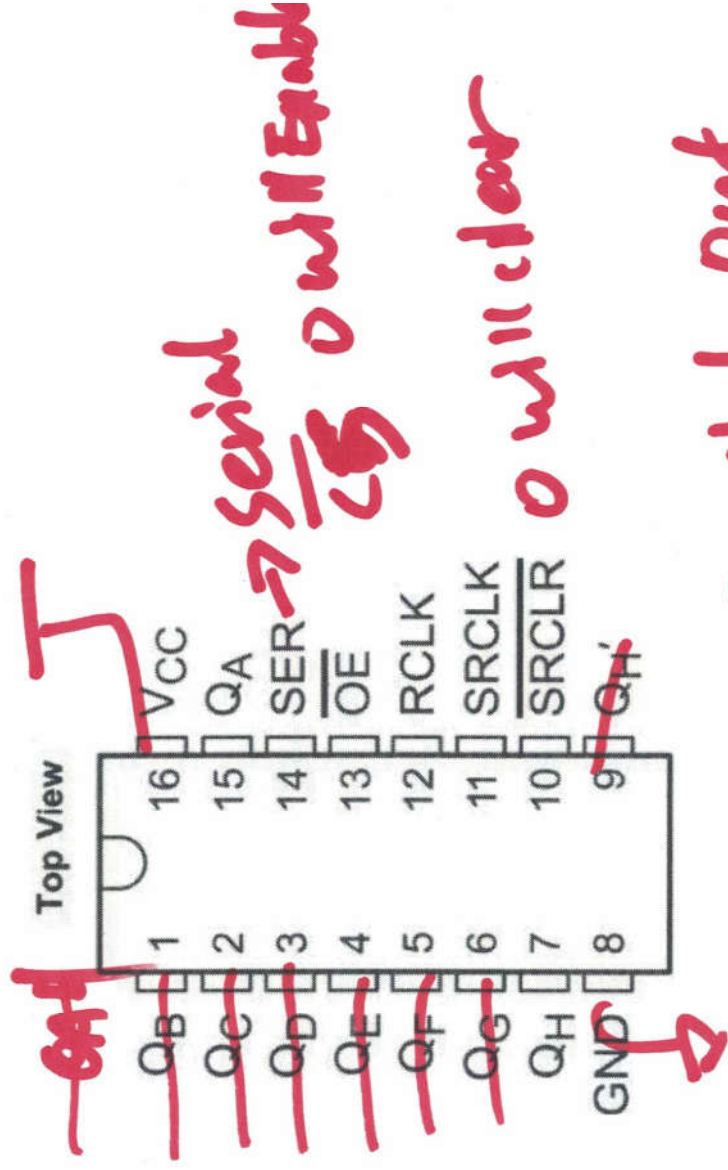


Serial  $\rightarrow$  Series

LSM-107



~~Serial Bus~~  
USB  
Universal  
Serial  
Bus



Serial-In-Parallel-Out

Q,

### 9.4 Device Functional Modes

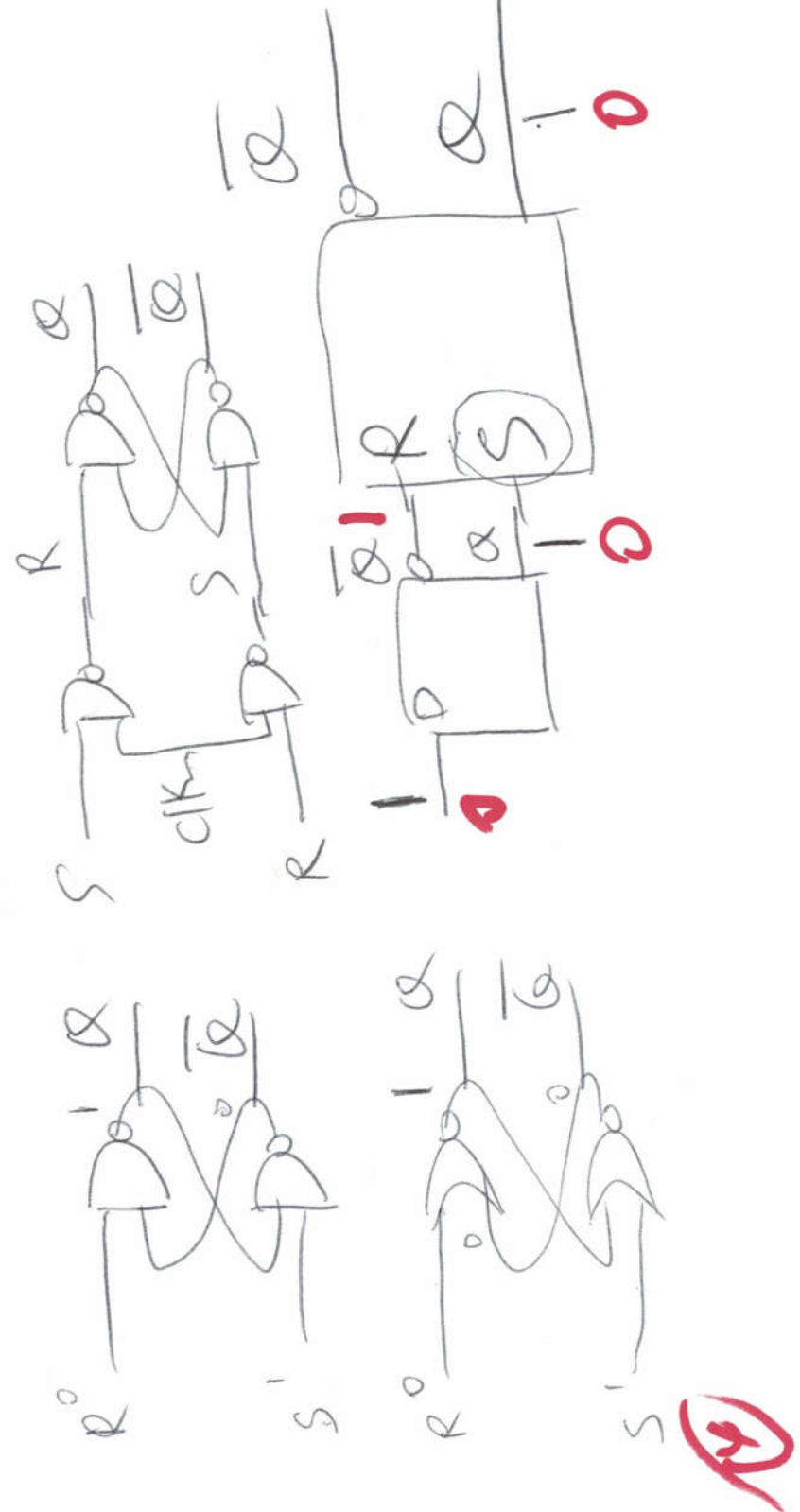
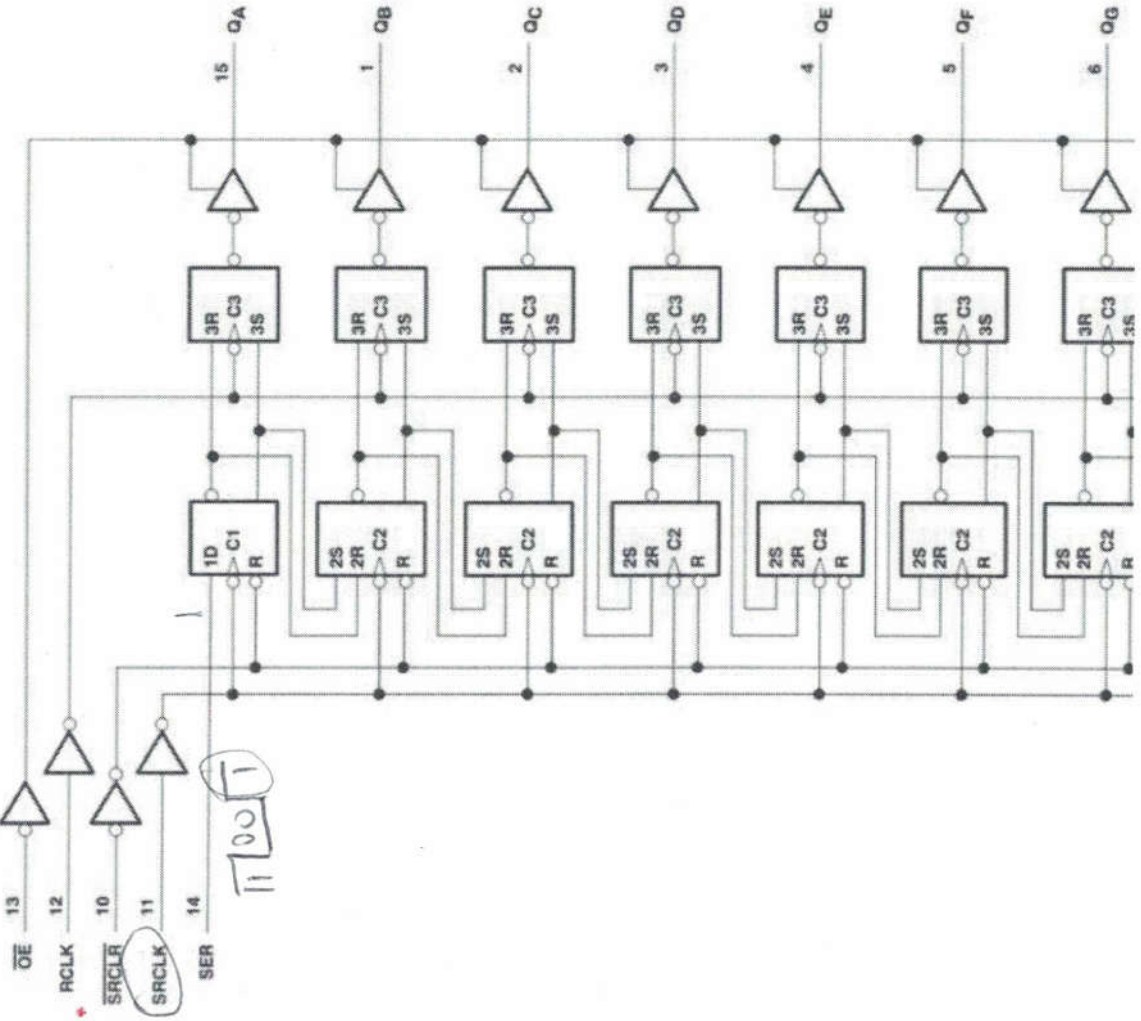
Table 1 lists the functional modes of the SNx4HC595 devices.

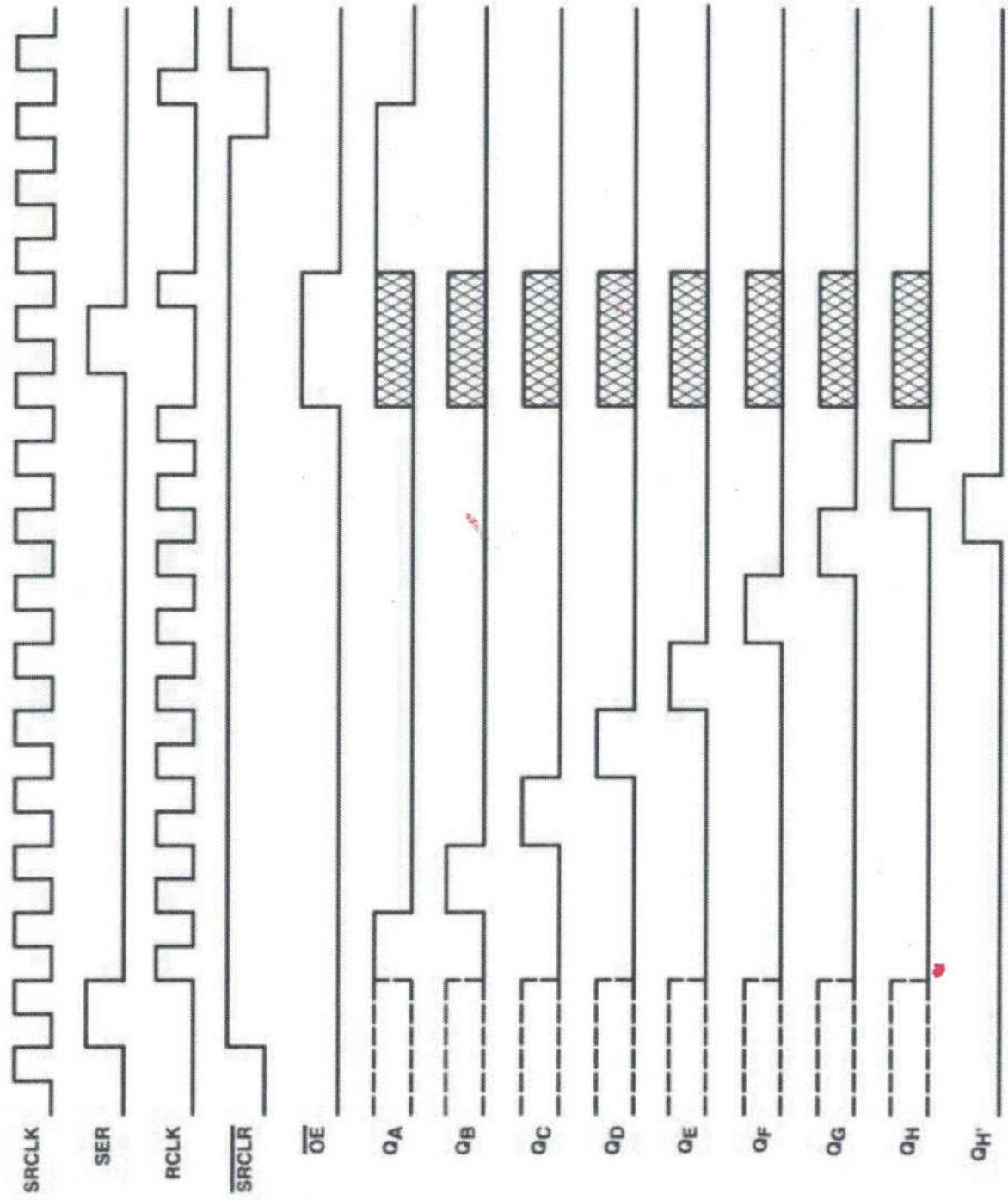
Table 1. Function Table

INPUTS					FUNCTION
SER	SRCLK	SRCLR	RCLK	$\overline{OE}$	
X	X	X	X	H	Outputs $Q_A - Q_H$ are disabled.
X	X	X	X	L	Outputs $Q_A - Q_H$ are enabled.
X	X	L	X	X	Shift register is cleared.
L	↑	H	X	X	First stage of the shift register goes low. Other stages store the data of previous stage, respectively.
H	↑	H	X	X	First stage of the shift register goes high. Other stages store the data of previous stage, respectively.
X	X	X	↑	X	Shift-register data is stored in the storage register.

↓ rising edge triggered







NOTE: [XXXXXXXXXX] implies that the output is in 3-State mode.

Serial-In-Parallel-Out

